



Department of Electrical & Electronics Engineering

Course Title: MICROPROCESSORS AND MICROCONTROLLERS LAB (GR20A3022)

Following documents are available in Course File.

S.No.	Points	Yes	No
1	Institute and Department Vision and Mission Statements	V	
2	Academic Calendar	$\sqrt{}$	
3	Subject Allocation Sheet	V	
4	Class Time Table, Individual Timetable (Single Sheet)	√	
5	Syllabus Copy	$\sqrt{}$	
6	Course Handout	V	
7	CO-PO Mapping	√	
8	Assignment Questions with CO's		√
9	Tutorial Sheets With Solution		√
10	Sessional Question Papers, External Question Paper and Scheme of Evaluation		V
11	Previous University Question Papers		$\sqrt{}$
12	Best, Average and Weak Answer Scripts for Each Sessional Exam. (Photocopies)	V	
13	CO-PO Attainments for All Mids.	√	
14	Soft Copy of Notes/Ppt/Slides		√
15	Feedback From Students	√	
16	Result Analysis	V	
17	Remedial Action.	V	
18	Course Exit Survey		√

Course Instructor / Course Coordinator

Course Instructor / Course Coordinator



Vision of the Institute

To be among the best of the institutions for engineers and technologists with attitudes, skills and knowledge and to become an epicentre of creative solutions.

Mission of the Institute

To achieve and impart quality education with an emphasis on practical skills and social relevance

Vision of the Department

To impart technical knowledge and skills required to succeed in life, career and help society to achieve self sufficiency.

Mission of the Department

- 1. To become an internationally leading department for higher learning.
- 2. To build upon the culture and values of universal science and contemporary education.
- 3. To be a center of research and education generating knowledge and technologies which lay groundwork in shaping the future in the fields of electrical and electronics engineering.
- 4. To develop partnership with industrial, R&D and government agencies and actively participate in conferences, technical and community activities.



Department of Electrical and Electronics Engineering

This Programme is meant to prepare our students to professionally thrive and to lead. During their progression:

Graduates will be able to

- **PEO 1**: Graduates will have a successful technical or professional careers, including supportive and leadership roles on multidisciplinary teams.
- **PEO 2**: Graduates will be able to acquire, use and develop skills as required for effective professional practices.
- **PEO 3**: Graduates will be able to attain holistic education that is an essential prerequisite for being a responsible member of society.
- **PEO 4**: Graduates will be engaged in life-long learning, to remain abreast in their profession and be leaders in our technologically vibrant society.

Programme Outcomes (B.Tech. – EEE)

At the end of the Programme, a graduate will have the ability to

- **PO-1:** Ability to apply knowledge of mathematics, science, and engineering.
- **PO-2:** Ability to identify, formulate, analyze engineering problems using engineering sciences.
- **PO-3:** Ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety..
- **PO-4:** Ability to design and conduct experiments, as well as to analyze and interpret data with valid conclusions.
- **PO-5:** Ability to utilize experimental, statistical and computational methods and tools necessary for modelling engineering activities.
- **PO-6:** Ability to apply reasoning informed by the relative knowledge to evaluate societal, health, safety, legal and cultural issues and tasks applicable to the professional engineering practice.
- **PO-7:** Ability to adapt broad education necessary to understand the impact of engineering solutions and obtain sustainability in a global, economic, environmental, and societal context.
- **PO-8:** Ability to discover ethical principles and bind to professional and ethical responsibility.
- **PO-9:** Ability to function as an individual and in multi-disciplinary teams.
- **PO-10:** Ability to communicate effectively on complex activities in engineering community and society.
- **PO-11:** Ability to develop Project management principles and apply in various disciplinary environments.
- PO-12: Recognition of the need for, and an ability to engage in life-long learning

Program Specific Outcomes(PSOs):

- **PSO-1**: Graduates will interpret data and able to analyze digital and analog systems related to electrical and programming them.
- **PSO-2**: Graduates will able to demonstrate, design and model electrical, electronic circuits, power electronics, power systems and electrical machines.



Gokaraju Rangaraju Institute of Engineering and Technology (Autonomous)

Bachupally, Kukatpally, Hyderabad – 500 090, India

GRIET/DAA/1H/G/22-23

19 July 2022

Academic Calendar Academic Year 2022-23

III B. Tech. – First Semester

S. No.	EVENT	PERIOD	DURATION
1	Commencement of First Semester class work	08-08-2022	
2	I Spell of Instructions	08-08-2022 to 08-10-2022	9 Weeks
3	I Mid-term Examinations	10-10-2022 to 13-10-2022	3 Days
4	II Spell of Instructions	14-10-2022 to 12-12-2022	9 Weeks
5	II Mid-term Examinations	13-12-2022 to 15-12-2022	3 Days
6	Preparation	16-12-2022 to 22-12-2022	1 Week
7	End Semester Examinations (Theory/ Practical) Regular/ Supplementary	23-12-2022 to 13-01-2023	3 Weeks
8	Commencement of Second Semester, AY 2022-23	16-01-2023	

III B.Tech. - Second Semester

S. No.	EVENT	PERIOD	DURATION
1	Commencement of Second Semester class work	16-01-2023	
2	I Spell of Instructions	16-01-2023 to 16-03-2023	9 Weeks
3	I Mid-term Examinations	17-03-2023 to 20-03-2023	3 Days
4	II Spell of Instructions	21-03-2023 to 29-04-2023	6 Weeks
5	Summer Vacation	01-05-2023 to 20-05-2023	3 Weeks
6	II Spell of Instructions Contd	22-05-2023 to 12-06-2023	3 Weeks
7	II Mid-term Examinations	13-06-2023 to 15-06-2023	3 Days
8	Preparation	16-06-2023 to 22-06-2023	1 Week
9	End Semester Examinations (Theory/ Practical) Regular / Supplementary	23-06-2023 to 15-07-2023	3 Weeks
10	Commencement of IV B.Tech First Semester, AY 2023-24	17-07-2023	

J. Pavean



Dean Academic Affairs



Gokaraju Rangaraju Institute of Engineering and Technology

Department of Electrical and Electronics Engineering

2022 -23 I sem Subject allocation sheet

II YEAR(GR20)	Se	ection-A		
Electrical Circuit Analysis	G Sandhya Rani			
Principles of Analog Electronics	P Ravikanth			
DC Machines and Transformers	Dr Phan	eedra Babu B		
Electromagnetic Fields	Dr T S	uresh Kumar		
Power Generation and Transmission	V Vijay	ya Rama Raju		
Java Programming for Engine	CSE	Dept. Staff		
Constitution of India	D Kar	runa Kumar		
Value Ethics and Gender Culture	MI	Prashanth		
Principles of Analog Electronics Lab	U Vijaya Lak	shmi/ M Prashanth		
DC Machines and Transformers Lab	V Vijaya Rar	na Raju / M Rekha		
III YEAR (GR20)	Se	ection-A		
Power System Analysis	Dr	J Sridevi		
Power Electronics	Dr Pakkiraiah B			
Microproces sors and Microcontrol lers	Dr D Raveedhra			
Electrical and Hybrid Vehicles (PE-1)	Dr D G Padhan			
Cloud Computing (NPTEL)	P Ravikanth			
Power Systems Lab	Dr J Sridevi / V Usha Rani/ U Vijaya Lakshmi			
Power Electronics Lab	Dr Pakkiraiah B/ G Sandhya Rani			
Microproces sors and Microcontrol lers Lab	Dr P Srividya Devi/ M N Sandhya Rani			
IV YEAR(GR18)	Section-A	Section-B		
Power Systems – III	Dr P Srividya Devi	P Prashanth Kumar		
Electronics Design	Dr D S N M Rao	Dr D S N M Rao		
Electrical and Hybrid Vehicles (PE-III)	D Srinivasa Rao	D Srinivasa Rao		
High Voltage Engineering (PE-IV)	A Vinay Kumar A Vinay Kumar			
Robotics	Anit	ha (Mech)		
Database Management Systems	D Swathi (CSE)			
Electronics Design Lab	P Ravikanth /Dr DSNM Rao	D Karuna Kumar/ V Usha Rani		
Project work - (Phasel)	A Vinay Kumar/ D Srinivasa Rao	M N Sandhya Rani / G Sandhya Rani		
I/I BEE(GR20)	Theory LAB			

EEE (1) BEE	
ECE (3) BEE	R Anil Kumar/ P Praveen Kumar / P Prashanth
IT (3) BEE	Kumar/ K Sudha
CSBS (1) PEE	
Design Thinking	Dr D G Padhan
Mech II/I (GR20)	А
BEEE	M N Sandhya Rani

Dr Phaneendra Babu B HOD,EEE



Gokaraju Rangaraju Institute of Engineering and Technology

Department of Electrical and Electronics Engineering

GRIET/PRIN/06/G/01/22-23

BTech - EEE - A

Wef : 08th Jul 2022 III Year - I Semester

DAY/ HOUR	9:00 - 9:55	9:55- 10:50	10:50 - 11:45	11:45 -12:25	12:25-1:15	1:15 - 2:05	2:05 -2:55		ROOM NO	
MONDAY	PE	PE	EHV		PE La	ab (A1)/PS Lab (Theory/Tutorial	4402		
TUESDAY	CC	МС	MC		PSA	PSA	Library	Lab	PE Lab (4405) MC Lab (4502)	
WEDNESDAY	МС	PSA	Mentoring	BREAK	PS La	b (A1)/MC Lab	(A2)	Lab	PS Lab (4504)	
THURSDAY	PSA	PSA	PE	DKLAK	MC L	ab (A1)/PE Lab	(A2)	Class Incharge:	G. Sandhya Rani	
FRIDAY	EHV	EHV	CC		Library	MC	MC			
SATURDAY	CC	PE	PE		Library	EHV	EHV			
Subject Code		Subject Name		Faculty Code	Faculty	Name	Almanac			
GR20A3012	Power	Systems Analysis	s (PSA)	Dr JSD	Dr J. Sridevi		1 st Spell of Instruc	ctions	08-08-2022 to 08-10-2022	
GR20A3013	Po	wer Electronics (PE)	Dr PB	Dr Pakki	raiah B	1 st Mid-term Examinations		10-10-2022 to 13-10-2022	
GR20A3014	Microprocess	ors and Microcon	ntrollers (MC)	Dr DR	Dr D Raveendhra		2 nd Spell of Instructions		14-10-2022 to 18-12-2022	
GR20A3015	Electrical	and Hybrid Vehi	cles (EHV)	Dr DGP	Dr D. G. Padhan		2 nd Mid-term Examinations		09-12-2022 to 13-12-2022	
	Clo	oud Computing (CC)	PRK	P. Ravikanth		Preparation		14-12-2022 to 20-12-2022	
GR20A3020	Power Systems Lab (PS Lab)		Dr JSD/ VUR/UVL	Dr J. Sridevi/ V. Usharani/ U. Vijayalakshmi		End Semester Examinations (Theory/		21-12-2022 to 10-01-2023		
GR20A3021	Power 1	Power Electronics Lab (PE Lab)		Dr PB/GSR/MRE	Dr. B. Pakkiraiah/G. Sandhya Rani/M Rekha		Practicals) Regula	ar / Supplementary	21 12 2022 to 10 01 2023	
GR20A3022	Microprocessor	rs and Microcont Lab)	rollers Lab (MC	Dr PSVD/MNSR	Dr. P. Srividya Devi/ M. N. Sandhya Rani		Commencement of A.Y 2022-2023	16-01-2023		

Faculty Name: Dr P Srividya Devi/M N Sandhya Rani									
DAY/ HOUR	09.00- 09.55	09.55- 10.50	10.50- 11.45	11.45- 12.25	12.25-02.55				
MONDAY									
TUESDAY									
WEDNESDAY				LUNCH	MPMC Lab(A2)				
THURSDAY				ІСН	MPMC Lab(A1)				
FRIDAY									
SATURDAY									



Department of Electrical & Electronics Engineering

COURSE OBJECTIVES

Academic Year : 2022-23

Semester : I

Name of the Program: B.Tech Year: III

Course/Subject: Microprocessors and Microcontrollers Lab Course Code: GR20A3022

Name of the Faculty: Dr P Srividya Devi/ M N Sandhya Rani Dept.:EEE

On completion of this Subject/Course the student shall be able to:

S.No	Objectives							
1	To introduce the basics of microprocessors and its applications.							
2	To provide in depth knowledge of 8051 Microcontrollers.							
3	To expertise working with programming.							
4	To impart the I/O interfacing concepts for developing real time systems.							
5	To encourage the students in building real time applications.							



Department of Electrical & Electronics Engineering

COURSE OUTCOMES

Academic Year : 2022-23

Semester : I

Name of the Program : B.Tech Year: III

Course/Subject: Microprocessors and Microcontrollers Lab

Course Code: GR20A3022

Name of the Faculty: Dr P Srividya Devi/ M N Sandhya Rani Dept.:EEE

The expected outcomes of the Course/Subject are:

S.No	Outcomes
1	Familiarize with the assembly level programming using 8086.
2	Judge the difference between Assembly language and Embedded C Programming
3	Design circuits for interfacing different modules to microcontrollers.
4	Experiment 8051 with different types of communicating devices.
5	Execute various programs which can resemble to the real time applications.



Department of Electrical & Electronics Engineering

GUIDELINES TO STUDY THE COURSE /SUBJECT

Academic Year : 2022-23

Semester : I

Name of the Program: B.Tech Year: III

Course/Subject: Microprocessors and Microcontrollers Lab Course Code: GR20A3022

Name of the Faculty: Dr P Srividya Devi/ M N Sandhya Rani Dept.:EEE

Guidelines to study the Course/ Subject: Microprocessors and Microcontrollers Lab

Course Design and Delivery System (CDD):

The Course syllabus is written into number of learning objectives and outcomes.

These learning objectives and outcomes will be achieved through lectures, assessments, assignments, experiments in the laboratory, projects, seminars, presentations, etc.

Every student will be given an assessment plan, criteria for assessment, scheme of evaluation and grading method.

The Learning Process will be carried out through assessments of Knowledge, Skills and Attitude by various methods and the students will be given guidance to refer to the text books, reference books, journals, etc.

The faculty be able to –

Understand the principles of Learning

Understand the psychology of students

Develop instructional objectives for a given topic

Prepare course, unit and lesson plans

Understand different methods of teaching and learning

Use appropriate teaching and learning aids

Plan and deliver lectures effectively

Provide feedback to students using various methods of Assessments and tools of Evaluation

Act as a guide, advisor, counselor, facilitator, motivator and not just as a teacher alone



Department of Electrical & Electronics Engineering

COURSE SCHEDULE

Academic Year : 2022-23

Semester : I

Name of the Program: B.Tech Year: III

Course/Subject: Microprocessors and Microcontrollers Lab Course Code: GR20A3022

Name of the Faculty: Dr P Srividya Devi/ M N Sandhya Rani Dept.:EEE

Sl.No	Date	Topics	No of periods
1	10/08/2022	Arithmetic Operations on 16 Bit and 32-Bit Data.	3
2	17/08/2022	Logical Operations on 16 Bit and 32-Bit Data.	3
3	24/08/2022	String Operations on 16 Bit and 32-Bit Data.	3
4	07/09/2022	Bit level Logical Operations	3
5	14/09/2022	Rotate, Shift Operations.	3
6	21/09/2022	Swap and Branch Operations.	3
7	28/09/2022	LED's to 8051.	3
8	12/10/2022	Interfacing LCD to 8051.	3
9	19/10/2022	Interfacing Matrix Keypad to 8051.	3
10	26/10/2022	Interfacing DC Motor to 8051.	3
11	02/11/2022	LEDs interfacing	3
12	09/11/2022	Switches and LED's interfacing	3
13	16/11/2022	2*16 LCD	3
14	23/11/2022	Serial Communication	3
15	30/11/2022	Device control	3
16	07/12/2022	Reading sensors using ADC	3
17	14/12/2022	DC Motor control	3



Department of Electrical & Electronics Engineering

EVALUATION STRATEGY

Academic Year : 2022-23

Semester : I

Name of the Program: B.Tech Year: III

Course/Subject: Microprocessors and Microcontrollers Lab Course Code: GR20A3022

Name of the Faculty: Dr P Srividya Devi/ M N Sandhya Rani Dept.:EEE

1. TARGET:

A) Percentage for pass: 99.7%

b) Percentage of class: 100%

2. COURSE PLAN & CONTENT DELIVERY:

- Observation & Record
- Doing the Experiments/Projects

3. METHOD OF EVALUATION

- 3.1 Continuous Assessment Examinations (CAE-I, CAE-II)
- 3.2 Projects
- 3.3 Quiz
- 3.4 Semester/End Examination



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CO-PO Mapping

	GR20A3022-MICROPROCESSORS AND MICROCONTROLLERS LAB													
COs/POs	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012	PSO1	PSO2
1. Familiarize with the														
assembly level		Н	Н		Η	Н		Н	M	M	Н	Н	Н	M
programming using 8086.														
2. Judge the difference														
between Assembly	Н	Н		M	Н	Н	M		M				Н	
language and Embedded C	11	11		171	11	11	171		171				11	
Programming														
3. Design circuits for														
interfacing different		Н	Н	M	Н		Н	M				Н	Н	
modules to		11	11	1V1	11		11	1V1				11	11	
microcontrollers.														
4. Experiment 8051 with														
different types of	Н	Н	Н				Н				M	M		Н
communicating devices.														
5. Execute various														
programs which can	M	Н	Н					Н	M	M		Н	Н	M
resemble to the real time	171	11	11					11	141	1 V1		11	11	171
applications.														



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COs	Cognitive Learning Levels									
	1	2	3	4	5	6				
1			\checkmark							
2		\checkmark								
3				\checkmark						
4						\checkmark				
5					\checkmark					

Cognitive Learning Levels

CLL 1: Remembering

CLL 2: Understanding

CLL 3: Applying

CLL 4: Analyzing

CLL 5: Evaluating

CLL 6: Creating



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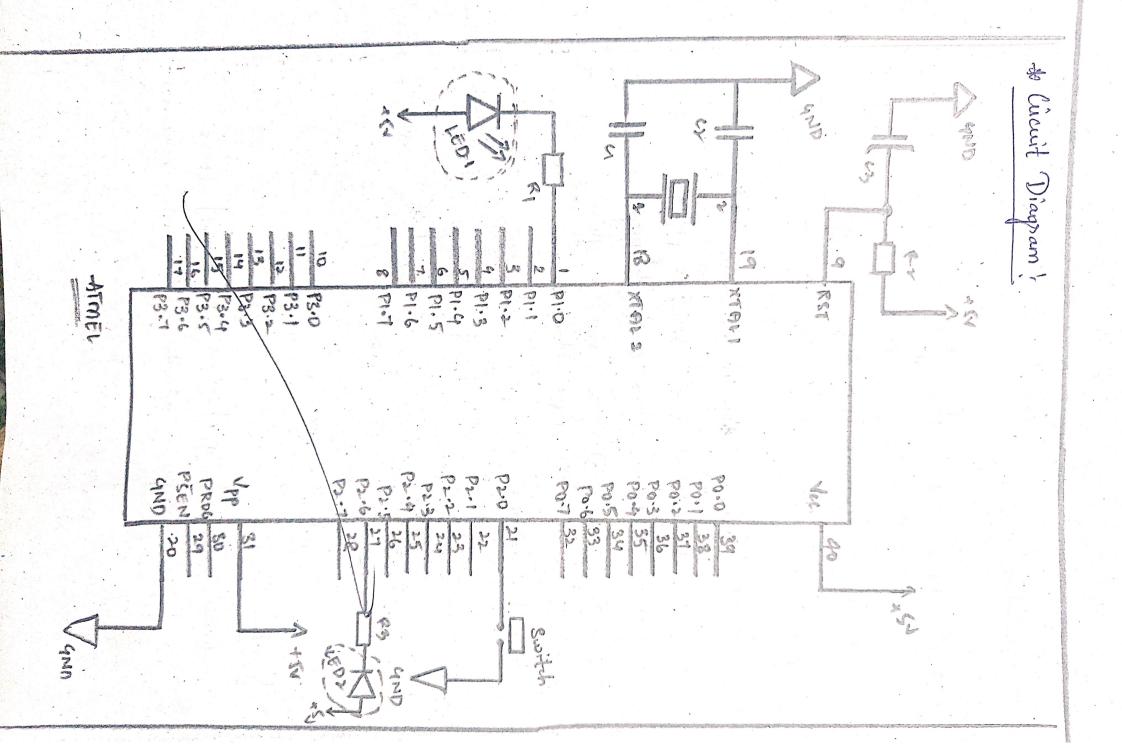
(8 Pages)

Bachupally, Kukatpally, Hyderabad - 500090

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			S	TART W	RITING	FRO	мн	ERE			(0			
	Appara 8051 Conne 2 LF	To oblicuo cont tu! micropating u p's.	serve roller.	blinking/805	ng of	with 80	proc	wir	ng:	AIN	رهح		olle.		

instruction.

- · Size of Micro Controller & Small when Compared to Microprocessor as it has internally connected Memory and to Ports.
- o when Certain Set of instructions are given to the Microproversors it processes and Encutes them within no time.
- * Some Known processors are, PNTEL, AMD, SNAPPRAYDN, etc.
- 1) Open the Einculation Software to perform the Emperiment
- 2) Carefully read all the instructions one by one.
- 8) elick on the show button to understand the given interface diagram and sample code.
- 4) Copy the Sample Code and parte it in the tent
- 5) Else, write your own arrembly language code in the tent editor.
- 6) To check the Syntam errors, debug uption is
 provided in the software.
- To case of any errore about on correct the errore and debug it again.
- 8) After debug it there are no errors, then run the code.
- a) Get the output.



Simulation program?

738.T 4.CLR CLR P2.6/(LED out P2.6 stops to glow PI. Of LED at PI.O chops glowing. P2. 6 / LED out Part glows. P1.0 / LED at P1.0 glows

(ii) during time - a LED at Dr. 6 starts to glow. + Observation? when we sun the program. i) during line-I LED at PIJO starts to glow. iii) during line - 3 LED at PIO iv) during line-4 yet at P2.6 stops to glow. maps of sdays ou shown

in Circuit Diagram.

& Result,

using somet ofice Henry, Sumerfully observed blinking of 160's Controlly. / 8057 M remo pround



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PRACTICAL EXAMINATION ANSWER BOOK INTERNAL
No. 52710 H.T. No. 2 0 2 4 1 A 0 2 1 6
Name of the Examination III-I Lab Internal Exam
Course Micro Gontrolless Lab Branch EEE Date 7/19/12
Signature of the Invigilator
START WRITING FROM HERE
Serial Communication - Read Analog Value
Aim: - Jo observe the Serial Communication by using drolling
Software
Appaeatus: - Maduino Software, Archuino UND board, Connectine
Wires.
Theory:
Sexial Communication is observed in Sexial Monitor. After
usition appeared to 11 serial Monitor. offer
writing appropriate code in the Editor window and running the program The output is the
Josephy ed in Serial Monitary
It consists Bordente of 9600. drawing board consists of
It consists Boderate of 9600. Archino board consists of 13 Analog Pins (2-7) are switches (8-13) are LED.

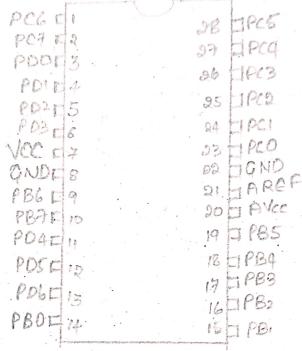
. and househt . Er

Procedure
1) Go to the Simulation Window in the software
2) Read the Instructions popping on the window.
3) Click on the sample code.
4) Copy the sample code from the window.
5) OR Write our own assembly language code
6) Select the port according to the code.
7) To find the Essors, debug the program by enabling
the debug option.
8) It any errors are found debug the function
9) To sun the program, see if there are any errors
and run it
10) Weite down the Result from Secial Monitor
Simulation Program
void setup()
Sevial-begin(9600);
가지 [1] 수 있었다. [2] 가게 하는 사람이 되었다면 하는 사람이 되었다. 그는 그 모든 사람이 되었다. 그는 사람이 되었다.
Void Loop ()
Serial print ln ("Hello World");
3

output: Hello World

Circuit Diagram:

PCG FI
POT FR



Arduino Pin Diagram

Result:

Hence the Sexial Communication-Read stralog Value has been studied by using Arduio Software



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I MACITAL EXAMINATION ANSWER BOOK INTERNAL
No. 52683 H.T. No. 2024110250
Name of the Examination TTING year 1st Sem Internal lab Examination.
Course MC Internal Exam Branch EEE Date 12/22
Signature of the Invigilator
START WRITING FROM HERE
6. Segral Communication - Read Dralog Value
Aim: - To determine the Sesial Communication on
Rand Analog Value.
Apparatus: - Social Communication
Analog kit
Motherboased kit
Brodes-Ardino Ono bourd
Communicator.
Procedure
1). Open the software micro processor.
2) Open the new page and Select the Arduino Uno board.

- 3) 5. 16 not clownloaded, download the Ardino Una board,
- en) intrite the program in the software of Andrimo
- 5) Debig Me program.
- 5) Ran Abre Save the program as some extended
- 6) Run the program
- (4) Connect the Communication.
 - () Switch on the Communication and start notes down the values.
 - q) In the Seired Communication note a table of

 Analog values.

Drog Som

Andry in out Senoul +

Count int analog in pm = Po; const not analog and pm dy: and sersol velue = 0) and butput value = 0) vord setupl) [2 Serial, Begm (9800);

word toop.

Butputt Sensor = 1023 output = 256 voltage 5.00 Sensor = 1023: out put = 255 voltage = 5.00

Result Hence the Selial Communication of Rad Dealog Values are obtained and waterlained,



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Direct Internal CO Attainments

Academic Year	2022-23		Departmen	nt	EEE						Name of the Programm		В.Т	ech					
Year - Semester	111-1		Course Na	me :	MPMC Lal	b]		Course Coo	le	GR20	A3022			Section	Α	
			•					Lab Int	ternal Exar	nination			•				Re	cord	Assessment
	Q.No 1	Q.No 2	Q.No 3	Q.No 4	Q.No 5	Q.No 6	Q.No 7	Q.No 8	Q.No 9	Q.No 10	Q.No 11	Q.No 12	Q.No 13	Q.No 14	Q.No 15	Viva	Iteration 1	Iteration 2	Marks
Enter CO Number → 1,2,3,4,5,6,7	1,2,3,4,5	1,2,3,4,5	1,2,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5				1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5
Marks →	10	10	10	10	10	10	10	10	10	10	10	10				10	5	5	5
S.No/Roll No.			Note : Ent	er Marks B	etween Tv	vo Green	rows. And	other Note	e : Additio	onal Colun	nns if Requ	ired should	d be inserte	ed after col	umn H and	appropriately rena	me the Q.	Nos.	
First / 1				8												10	5	5	5
2						8										9	5	5	4
3			7													10	5	5	5
4	10															10	5	5	5
5							10									10	5	5	5
6									9							9	5	5	4
7		3														10	5	5	5
8					3											10	5	5	5
9										3						8	5	5	3
10											10					10	5	5	5
11									9							10	5	5	5
12												9				10	5	5	5
13				5												7	4	4	3
14		9														10	5	5	5
15	6															9	5	5	4
16	 		8													9	5	5	4
17			<u> </u>		10											10	5	5	5
18					10		6									10	5	5	5
19				6												10	5	5	5
20		3		-												9	5	5	4
21		-				3										10	5	5	5
22						,		7								9	4	4	5
23				 					3				 			8	3	3	5
24				 					3			3	 			8	5	5	3
25		 		 	 				-	9		-	 			9	4	4	5
26				-	-		5		-	3						8	5	5	3
26	1			-	6		3		-							9	5	5	4
27	1	-		-	°								-				5	5	
	-	-		9	-				-				-			10			5
29	 _	6		-									-			9	5	5	4
30	2			-					-				-			6	3	3	3
31				-	-				-				-			0	0	0	0
32		9			-											10	5	5	5
33				3		_										8	4	4	4
34						9										9	5	5	4
35								6								9	5	5	4

27																				
38	36										9						7	3	3	4
Section Sect	37												9				7	3	3	4
40	38											9					10	5	5	5
41	39									9							10	5	5	5
## 42	40							9									8	5	5	3
## 44	41						6										10	5	5	5
## 44	42				8												8	4	4	4
45 3	43			4													6	3	3	3
45 3	44		8														10	5	5	5
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Solution	57					10											10	5	5	5
60	58							8									9	5	5	4
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62 63 64 7 8 8 9 8 65 9 8 65 65 65 65 65 65 65 65 65 65 65 65 65	60									8							10	5	5	5
62 63 64 7 8 8 9 8 65 9 8 65 65 65 65 65 65 65 65 65 65 65 65 65	61											10					10	5	5	5
63 64 7 8 8 9 8 64 9 8 65 9 8 65 65 65 65 65 65 65 65 65 65 65 65 65													8					-		5
Figure F							8													4
Figure Class strength is > 60 then Insert rows above the green row Lost record Similarly delete the empty rows above green row if the class strength is < 60 Total number of students S 8 4 8 6 5 7 2 6 4 4 5 65 65 65					7															4
If your class strength is > 60 then <u>insert rows_above the green row Lost record</u> , Similarly <u>delete the empty rows above green row</u> if the class strength is < 60) Total number of students appeared for the examination (NST) Total number of students attempted the question (NST) Total number of students attempted the question (NST) Total number of students attempted the question (NSA) Attempte (TAP) = (NSA/NST)*100 Total number of Students who got more than 60% Attempte (TAP) = (NSA/NST)*100 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 60.00 Total number of Students who got more than 60% Attainment (STMP) = 6			0		,															4
Total number of students appeared for the examination (NST) Total number of students attempted the question 5 8 4 8 6 5 7 2 6 4 4 5 65 65 65 65 65 65 65 65 65 65 65 65 6	- 55																			-
appeared for the examination (NST) Total number of students attempted the question (NST) Total number of students attempted the question (NSA) Attempts (TAP) = (NSA/NST)*100 Total number of Students who got more than 60% as 6 3 6 5 4 5 2 4 3 3 4 664 64 64 64 64 64 64 64 64 64 64 64			if your c	lass strengt	th is > 60 th	nen <u>insert</u>	rows <u>abo</u>	ve the gre	en row La	<u>ist record</u>	, Similarly	delete the	empty rov	vs above gr	<u>reen row</u> if	the class s	trenght is < 60)			
attempted the question (NSA) Attempt % (TAP) = (100.00 10	appeared for the	5	8	4	8	6	5	7	2	6	4	4	5				65	65	65	65
(NSA/NST)*100	attempted the question	5	8	4	8	6	5	7	2	6	4	4	5				65	65	65	65
who got more than 60% marks (NSM) Attainment % (TMP) = (60.00 75.00 75.00 75.00 83.33 80.00 71.43 100.00 66.67 75.00 75.00 80.00 98.46 98.46 98.46 98.46 Score(S) Note: CO attainment is considered to be zero if the attempt % is less than 30% CO Validation 1,2,3,4,5 1,2,3,4,		100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00				100.00	100.00	100.00	100.00
Note : CO attainment is considered to be zero if the attempt % is less than 30%	who got more than 60%	3	6	3	6	5	4	5	2	4	3	3	4				64	64	64	64
Note : CO attainment is considered to be zero if the attempt % is less than 30% CO Validation 1,2,3,4,5 1		60.00	75.00	75.00	75.00	83.33	80.00	71.43	100.00	66.67	75.00	75.00	80.00				98.46	98.46	98.46	98.46
CO Validation 1,2,3,4,5 1,	Score(S)	3	3	3	3	3	3	3	3	3	3	3	3				3	3	3	3
Course Outcome 02,C03,C002,C03,C0C01,C02,C05C02,C03,C0C0							Note : 0	O attainm	ent is cons	idered to b	e zero if th	ne attempt 9	% is less tha	n 30%						
Course Outcome 02,C03,C0 02,C0	CO Validation	1,2,3,4,5	1,2,3,4,5	1,2,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5				1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5
	Course Outcome				CO2,CO3,CO4	02,C03,C0								4			01,C02,C03,C04,CC	CO3,CO4,	CO3,CO4,	CO1,CO2,CO3,C O4,CO5
	Marks (Y)	10	10	10	10	10	10	10	10	10	10	10	10				10			5
No. of COs Shared (Z) 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	No. of COs Shared (Z)	5	5	3	5	5	5	5	5	5	5	5	5				5	5	5	5

Y/Z	2	2	3.333333	2	2	2	2	2	2	2	2	2		2	1	1	1
S*Y/Z	6	6	10	6	6	6	6	6	6	6	6	6		6	3	3	3
	-	-					-	-									
CO1	1	1	1	1	1	1	1	1	1	1	1	1		1	1	1	1
CO2	1	1	1	1	1	1	1	1	1	1	1	1		1	1	1	1
CO3	1	1	0	1	1	1	1	1	1	1	1	1		1	1	1	1
CO4	1	1	0	1	1	1	1	1	1	1	1	1		1	1	1	1
CO5	1	1	1	1	1	1	1	1	1	1	1	1		1	1	1	1
CO6	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0	0
CO7	0	0	0	0	0	0	0	0	0	0	0	0		0	0	0	0

Weighted Average for	CO1	CO2	CO3	CO4	CO5	CO6	CO7
Attainment relevance (Internal CODn)	3.00	3.00	3.00	3.00	3.00		

^{!!} Caution !! For CO Values < 2.1 should be justified with Remidial Action Report.



Gokaraju Rangaraju Institute of Engineering and Technology

(Autonomous)

Bachupally, Kukatpally, Hyderabad – 500 090 Indirect CO Attainments

2022-23 Academic Year Year - Semester

Department	EEE
Course Name :	MPMC Lab

B.Tech
GR20A3022 Section A

Course Or	itcomes surv	ev on Scale	1 (I ow) to	5 (High)

Course Outcome→	Familiarize with the assembly level programming using 8086.	2. Judge the difference between Assembly language and Embedded C Programming	3. Design circuits for interfacing different modules to microcontrollers.	4. Experiment 8051 with different types of communicating devices.	5. Execute various programs which can resemble to the real time applications.		
CO Number 1,2,3,4,5,6,7	1	2	3	4	5		
Marks	5	5	5	5	5		
S.No/Roll No.	,			er Marks Between Two G			
First record/1	5	5	5	5	4		
Pirst record/1	4	4	5	5	5		
3	4	4	3	5	4		
4	4	4	4	3	4		
5	5	5	3	5	5		
6	5	4	5	5	5		
7	3	3	5	5	4		
8	5	5	5	4	5		
9	5 4	5	5	5	5		
11	5	4	3	5	5		
12	3	5	4	4	5		
13	5	5	5	4	5		
14	5	5	5	5	4		
15	4	5	4	4	5		
16	5	5	5	5	5		
17	5	5	5	5	5		
18	5	5	5	4	5		
19 20	5	4	5	4	5		
20	5	5	5	5	5		
21 22	5	4	5	5	5		
23	5	5	4	5	4		
24	5	4	5	5	5		
25	5	5	5	5	5		
26	5	5	5	5	4		
27	5	4	4	4	4		
28	4	5	4	5	5		
29	4	5	5	5	5		
30	5	4	5	5	4		
31	5	5	5	5	5		
32 33	4	5	5	5	5		
34	5	4	5	4	5 4		
35	5	5	5	5	5		
36	4	5	4	5	5		
37	4	5	5	5	5		
38	5	5	5	4	5		
39	5	5	5	5	5		
40	5	5	5	4	5		
41	5	5	4	5	4		
42	5	5	5	5	5		
43	5	5	5	5	5		
45	4	4	5	5	5		
46	4	5	5	5	4		
47	5	5	5	5	5		
48	4	4	5	5	5		
49	4	5	5	5	5		
50	4	5	4	5	4		
51	4	5	5	5	5		
52	5	5	4	3	5		
53	4	4	4	5	4		
54 55	5	5	3 5	5	5		
56	5	5	5	5	4		
57	5	5	5	5	4		
58	5	5	5	5	4		
59	5	5	5	5	4		
60	4	5	5	5	5		
61	5	5	4	3	5		
62	4	4	4	5	4		
63	5	4	3	4	5		
64	5	5	5	5	4		
Last Record/65	5	5	5	5	4		
if you	ır class strength is > 60 th	nen <u>insert rows</u> <u>above the</u>	e green row Last record ,	Similarly <u>delete the</u> <u>emp</u>	ty rows above green row	if the class strenght is <	60)
Total number of students							
appeared for the examination (NST) Total number of students	65	65	65	65	65		
attempted the question (NSA) Attempt % (TAP) =	100.00	100.00	100.00	100.00	100.00		
(NSA/NST)*100 Total number of Students who got more than 60%	65	65	65	65	65		
marks (NSM) Attainment % (TMP) =	100.00	100.00	100.00	100.00	100.00		
(NSM/NSA)*100 Score(S)	3	3	3	3	3		
CO attainment is considered							

CO attainment is considered zero if the attempt % is less than 30%

Indirect CO (COIn)	CO1	CO2	CO3	CO4	CO5	
marrect co (com)	3	3	3	3	3	

!! Caution !! For CO Values < 2.1 should be justified with Remidial Action Report.



Gokaraju Rangaraju Institute of Engineering and Technology (Autonomous) Bachupally, Kukatpally, Hyderabad – 500 090

Direct External CO Attainmen	

Academic Year Year - Semester	2022-23 III-I		Department				EE IC Lab			Name Progra Cours	amme	B.T GR20.	ech A3022		Section	A
					Par	t A			I 					Part B		
Enter CO Number →	Q.No 1	Q.No 2	Q.No 3	Q.No 4	Q.No 5	Q.No 6	Q.No 7	Q.No 8	Q.No 9	Q.No 10	Q.No 11	Q.No 12	Q.No 13	Q.No 14	Q.No 15	Viva
1,2,3,4,5,6,7	1,2,3,4,5	1,2,3,4,5	1,2,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5				1,2,3,4,5
Marks → S.No/Roll No.	50 Note: Er	50 Iter Marks	50 S Between	50 Two Gree	50 en rows. <u>A</u>	50 nother No	50 ote: Add	50 tional Col	50 umns if Re	50 equired sh	50 ould be in	50 serted aft	er column	H and ap	propriatel	20 y rename the
First record / 1			22					Q	. Nos.							20
3		15			22											18 20
4 5	42					43										20 20
6						4.5		22								18
7 8									22	25						20
9				30							23					16 20
11 12		33			32											20 20
13 14							21		22							14
15			34						22							18
16 17	30			28												18 20
18 19						24		25								20 20
20 21			14	24												18 20
22						29										18
23 24		16							38							16 16
25 26	31			36												18 16
27 28					26		29									18
29							23		29	20						18
30 31										20						12 AB
32 33											44	17				20 16
34 35										24		34				18 18
36								29								14
37 38				33		34										20
39 40	30	44														20 16
41 42					34		36									20 16
43							30			32						12
44 45								26				33				20 18
46 47					46		33									18
48 49			24	28												18 16
50		24	2.7													14
51 52				33	28											20 14
53 54	18						47									20 14
55 56		28	44													18 20
57			-	38												20
58 59					33	35										18 16
60 61							44	37								20
62 63									37	32						20 18
64 Last record/65											15	34				18
Last record/65	if your cla	ss strength	is > 60 ther	insert row	s above the	green row	Last record	, Similarly <u>c</u>	lelete the e	npty rows a	bove green		class streng	ht is < 60)		18
Total number of students appeared for the examination	5	6	5	8	7	5	6	5	5	5	3	4				65
(NST) Total number of students																
attempted the question (NSA)	5	6	5	8	7	5	6	5	5	5	3	4				64
Attempt % (TAP) = (NSA/NST)*100 Total number of Students	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00	100.00				98.46
who got more than 60% marks (NSM)	4	2	2	5	4	3	4	1	2	2	1	3				64
Attainment % (TMP) = (NSM/NSA)*100	80.00	33.33	40.00	62.50	57.14	60.00	66.67	20.00	40.00	40.00	33.33	75.00				100.00
Score(S)	3	1	1	3	2 CO attair	3 nment is con	3 sidered zero	0 if the attem	1 pt % is less t	1 nan 30%	1	3				3
CO Validation	1,2,3,4,5	1,2,3,4,5	1,2,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5	1,2,3,4,5				1,2,3,4,5
	-,-,-,-,															CO1,CO2,CO3,C
		-03 603 60	-01 603 60	.03.003.00	-03 603 60	.03 603 60	-03 603 60		-03 603 60		-03 -03 -0	-03 -03 -0				
Course Outcome		02,003,00	co1,co2,co	:02,C03,C0	:02,003,00	CO2,CO3,CO	:02,C03,C0	co2,co3,co	:02,C03,C0	:02,003,00	co2,co3,co	:02,C03,C0				04,005
Course Outcome Marks (Y)		50	50	50	50	50	50 50	co2,co3,co 50	50 50	50	50	50				
Marks (Y) No. of COs Shared (Z)	50 5	50	50	50	50	50	50	50	50	50	50	50				04,CO5 20 5
Marks (Y) No. of COs Shared (Z) Y/Z	50 5	50 5	50 3 16.666667	50 5	50 5	50 5	50 5	50 5	50 5	50 5	50 5	50 5				04,C05 20 5 4
Marks (Y) No. of COs Shared (Z)	50 5	50	50	50	50	50	50	50	50	50	50	50				04,CO5 20 5
Marks (Y) No. of COs Shared (Z) Y/Z S*Y/Z	50 5 10	50 5 10	50 3 16.666667 16.666667	50 5 10 30	50 5 10 20	50 5 10 30	50 5 10 30	50 5 10 0	50 5 10 10	50 5 10 10	50 5 10	50 5 10 30				04,C05 20 5 4
Marks (Y) No. of COs Shared (Z) Y/Z S*Y/Z	50 5 10 30	50 5 10 10	50 3 16.666667 16.666667	50 5 10 30	50 5 10 20	50 5 10 30	50 5 10 30	50 5 10 0	50 5 10 10	50 5 10 10	50 5 10 10	50 5 10 30				04,C05 20 5 4 12
Marks (Y) No. of COs Shared (Z) Y/Z S*Y/Z CO1 CO2	50 5 10 30	50 5 10 10	50 3 16.666667 16.666667	50 5 10 30	50 5 10 20	50 5 10 30	50 5 10 30	50 5 10 0	50 5 10 10	50 5 10 10	50 5 10 10	50 5 10 30				04,COS 20 5 4 12 1 1
Marks (Y) No. of COs Shared (Z) Y/Z S*Y/Z	50 5 10 30	50 5 10 10	50 3 16.666667 16.666667	50 5 10 30	50 5 10 20	50 5 10 30	50 5 10 30	50 5 10 0	50 5 10 10	50 5 10 10	50 5 10 10	50 5 10 30				04,C05 20 5 4 12
Marks (Y) No. of CO2 Shared (Z) Y/Z 5*Y/Z CO1 CO2 CO3	50 5 10 30	50 5 10 10 1	1 1 0	50 5 10 30 1 1	50 5 10 20 1 1	50 5 10 30 1 1	50 5 10 30 1 1	50 5 10 0	50 5 10 10 1	50 5 10 10 1 1	50 5 10 10	50 5 10 30 1 1				04,COS 20 5 4 12 1 1 1 1
Marks (Y) No. of COs shared (Z) Y/Z S*Y/Z CO1 CO2 CO3 CO4	50 5 10 30	50 5 10 10 1 1 1	50 3 16.666667 16.666667	50 5 10 30 1 1 1	50 5 10 20 1 1 1	50 5 10 30 1 1 1	50 5 10 30 1 1 1	50 5 10 0	50 5 10 10 1 1 1	50 5 10 10 1 1 1 1	50 5 10 10 1 1 1	50 5 10 30 1 1 1				04,C05 20 5 4 12 1 1 1
Marks (Y) No. of CO2 shared (Z) Y/Z S*Y/Z CO1 CO2 CO3 CO4 CO5	50 50 5 10 30	50 5 10 10 1 1 1 1 1	50 3 16.666667 16.666667 1 1 0	50 5 10 30 1 1 1 1	50 5 10 20 1 1 1 1	50 5 10 30 1 1 1 1	50 5 10 30 1 1 1 1	50 5 10 0 1 1 1 1	50 5 10 10 1 1 1 1 1	50 5 10 10 1 1 1 1 1	50 5 10 10 1 1 1 1	50 5 10 30 1 1 1 1				04,C05 20 5 4 12 1 1 1 1
Marks (Y) No. of COs Shared (Z) Y/Z S*Y/Z CO1 CO2 CO3 CO4 CO5 CO6	50 5 18 30 1 1 1 1	50 5 10 10 1 1 1 1 1 1	50 3 16.666667 16.666667 1 1 0 0	50 5 10 30 1 1 1 1 1	50 5 10 20 1 1 1 1 1	50 5 10 30 1 1 1 1 1	50 5 10 30 1 1 1 1 1	50 5 10 0 1 1 1 1 1 0	50 5 10 10 1 1 1 1 1 1	50 5 10 10 1 1 1 1 1 1	50 5 10 10 1 1 1 1 1 1	50 5 10 30 1 1 1 1 1				04,005 20 5 4 12 1 1 1 0



Gokaraju Rangaraju Institute of Engineering and Technology

(Autonomous)

Bachupally, Kukatpally, Hyderabad – 500 090

Summary Sheet CO Attainments

Academic Year:	2022-23
Course/Subject:	MPMC Lab
Department:	EEE
Section	A

Name of the Program:	B.Tech
Course Code:	GR20A3022
Year - Semester :	III-I

Attainment/CO	CO1	CO2	CO3	CO4	CO5	CO6	CO7
Attainment for Direct Internal CO (Mid I & II, Assignments, Tutorials, Assessments, etc.)	3.00	3.00	3.00	3.00	3.00		
Attainment for Direct External CO (End Semester Exam)	1.83	1.83	1.95	1.95	1.83		
Direct CO (0.3*Internal + 0.7*External)	2.18	2.18	2.26	2.26	2.18		
Indirect CO	3.00	3.00	3.00	3.00	3.00		
Final CO (COFn) = (0.9 x Direct CO + 0.1 x Indirect CO)	2.26	2.26	2.34	2.34	2.26		

со	Course Outcome	Remedial Action for COs Less than 70% (2.10)
CO1	1. Familiarize with the assembly level programming using 8086.	
CO2	Judge the difference between Assembly language and Embedded C Programming	
соз	3. Design circuits for interfacing different modules to microcontrollers.	
CO4	4. Experiment 8051 with different types of communicating devices.	
CO5	5. Execute various programs which can resemble to the real time applications.	
CO6		
CO7		

ID No.	Name of the Faculty	Department	Signature
931	Dr P Srividya Devi	EEE	
882	M N Sandhya Rani	EEE	

HOD DAA

Copy to: IQAC



Gokaraju Rangaraju Institute of Engineering and Technology

(Autonomous)

Bachupally, Kukatpally, Hyderabad - 500 090

Direct Internal CO Attainments

Academic Year	2022-23
Year - Semester	III-I

Department	EEE
Course Name :	MPMC Lab

P-Outcomes	A	В	С	D	Е	F	G	Н	ī	I	K	L	PSO1	PSO2
C-Outcomes)	,	1	•			-	,	,			. 552
1	Н	M	Н		M		M	Н	M	M	Н	Н	М	н
2	Н	Н	Н		M		M	Н	M		Н	Н	М	Н
3	Н	Н	Н	Н	M		M	Н		M	M	Н	М	н
4	Н	Н	Н			М	M				Н	Н	М	Н
5	Н	Н	Н	M		M		M			Н	Н	М	Н
6														
7														

Convert above mappings to scale 1-3

P-Outcomes C-Outcomes	A	В	С	D	Е	F	G	Н	I	J	K	L	PSO1	PSO2
CO1	3	2	3		2		2	3	2	2	3	3	2	3
CO2	3	3	3		2		2	3	2		3	3	2	3
CO3	3	3	3	3	2		2	3		2	2	3	2	3
CO4	3	3	3			2	2				3	3	2	3
CO5	3	3	3	2		2		2			3	3	2	3
CO6														
CO7														
Expected Attainment	3.00	2.80	3.00	2.50	2.00	2.00	2.00	2.75	2.00	2.00	2.80	3.00	2.00	3.00

Final Cos CoF

CO1	CO2	соз	CO4	CO5	CO6	CO7
2.26	2.26	2.34	2.34	2.26		

	Attained PO A	Attained PO B	Attained PO C	Attained PO D	Attained PO E	Attained PO F	Attained PO G	Attained PO H	Attained PO I	Attained PO J	Attained PO K	Attained PO L	PSO1	PSO2
CO1	2.26	1.51	2.26		1.51		1.51	2.26	1.51	1.51	2.26	2.26	1.51	2.26
CO2	2.26	2.26	2.26		1.51		1.51	2.26	1.51		2.26	2.26	1.51	2.26

Name of the Programme	B.Tech
Course Code	GR20A3022

Enter H,M, L values of CO-PO Mapping Matrix in blue shaded rows 12 - 18 for seven CO s automatically PO **Attainments are Calculated** \leftarrow



CO3	2.34	2.34	2.34	2.34	1.56		1.56	2.34		1.56	1.56	2.34	1.51	2.26
CO4	2.34	2.34	2.34			1.56	1.56				2.34	2.34	1.51	2.26
CO5	2.26	2.26	2.26	1.51		1.51		1.51			2.26	2.26	1.51	2.26
CO6														
CO7														
Attained	2.29	2.14	2.29	1.92	1.52	1.53	1.53	2.09	1.51	1.53	2.14	2.29	1.51	2.26

	Α	В	С	D	E	F	G	н	1	J	К	L	PSO1	PSO2
	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	7301	1302
Expected	3.00	2.80	3.00	2.50	2.00	2.00	2.00	2.75	2.00	2.00	2.80	3.00	2.00	3.00
Attained	2.29	2.14	2.29	1.92	1.52	1.53	1.53	2.09	1.51	1.53	2.14	2.29	1.51	2.26
Percentage %	76.37	76.44	76.37	76.88	76.20	76.63	76.63	76.05	75.36	76.63	76.26	76.37	75.36	75.36

Note: PO is Satisfied if attained PO > 70, U indicates PO Unsatisfied

Faculty Co-Ordinator HOD



Approved By AICTE, Affiliated to JNTUH, Autonomous Under UGC Nizampet Road, Bachupally, Kukatpally, Hyderabad - 500090, Telangana, India Tel: 7207344440, Email:info@griet.ac.in, www.griet.ac.in

STUDENT FEEDBACK

Faculty : PALAKALURI. SRIVIDYA DEVI

Subject : Microprocessors And Microcontrollers Lab (B.Tech, III/IV B.Tech I Semester, EEE Sec-A)

Academic Year : 2022 - 2023 Phase : Phase-3

SI.No	Question	Excellent	Good	Average	Poor	Q.Wise Total	Q.Wise %
1	Preparation and delivery of the lessons by the teacher	18	34	4	2	184	79.00
2	Subject Knowledge	17	34	5	2	182	78.00
3	Clarity in Communication	20	32	3	3	185	80.00
4	Using Modern Teaching Aids of ICT	17	37	1	3	184	79.00
5	Creating interest on the course in the class	17	35	4	2	183	79.00
6	Maintaining discipline in the class	14	39	3	2	181	78.00
7	Encouraging and clearing doubts in the class	15	35	6	2	179	77.00
8	Punctuality	14	38	4	2	180	78.00
9	Accessibility of the teacher	13	40	2	3	179	77.00
10	Overall grading of the teacher	17	36	3	2	184	79.00
	Total	162	360	35	23		
	Total Points	648	1080	70	23	1821	78.00

No.Of Students Posted	58
Total Percentage Awarded to The Faculty	78.00
Grade of Faculty	Good

*Excellent (4): >=90 % *Good (3): >=75 & <90% *Average (2): >=60 & <75 % *Poor (1): Below 60 %

Formula: Total Obtained Points/(Max Points(i.Excellent-4) * No.Of.Students * NoOfQuestions)



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STUDENT FEEDBACK

Faculty: MULAGAPATI. NAGA SANDHYA RANI

Subject : Microprocessors And Microcontrollers Lab (B.Tech, III/IV B.Tech I Semester, EEE Sec-A)

Academic Year : 2022 - 2023 Phase : Phase-3

SI.No	Question	Excellent	Good	Average	Poor	Q.Wise Total	Q.Wise %
1	Preparation and delivery of the lessons by the teacher	17	36	3	2	184	79.00
2	Subject Knowledge	16	38	2	2	184	79.00
3	Clarity in Communication	15	37	4	2	181	78.00
4	Using Modern Teaching Aids of ICT	18	33	4	3	182	78.00
5	Creating interest on the course in the class	16	34	6	2	180	78.00
6	Maintaining discipline in the class	17	36	3	2	184	79.00
7	Encouraging and clearing doubts in the class	18	31	6	3	180	78.00
8	Punctuality	15	33	8	2	177	76.00
9	Accessibility of the teacher	15	37	2	4	179	77.00
10	Overall grading of the teacher	16	36	3	3	181	78.00
	Total	163	351	41	25		
	Total Points	652	1053	82	25	1812	78.00

No.Of Students Posted	58
Total Percentage Awarded to The Faculty	78.00
Grade of Faculty	Good

*Excellent (4): >=90 % *Good (3): >=75 & <90% *Average (2): >=60 & <75 % *Poor (1): Below 60 %

Formula: Total Obtained Points/(Max Points(i.Excellent-4) * No.Of.Students * NoOfQuestions)



Gokaraju Rangaraju Institute of Engineering & Technology

III B.Tech I Sem (EEE) Result Analysis

Academic Year: 2022-23 Total No. of Students Registered: 65

G	Total No.	Total No. of	No. of	Count of Students with Grade Point								
Course	of Students appeared	Students Passed	Students Failed	GP (10)	GP (9)	GP (8)	GP (7)	GP (6)	GP (5)			
PSA	65	50	15	00	00	12	10	14	14			
PE	65	50	15	00	02	06	12	19	11			
MPMC	65	47	18	00	01	09	08	12	17			
EHV	65	60	05	00	05	14	20	15	06			
PS-I Lab	65	63	02	10	19	07	03	13	11			
PE Lab	65	64	01	22	11	15	10	06	00			
MPMC Lab	65	64	01	07	11	21	16	08	01			
Cloud Computing (MOOCs)												

Arrears Position – III year / I Semester

No.of students	All Pass	One Arrear	Two Arrears	Three Arrears	More than three arrears	Over all %
						of pass
65	38	11	09	03	04	58.46 %

Performance overall Class Three Toppers

ROLL NO.	NAME	SGP
		A
20241A0235	RAMINENI VYSHNAVI	
		8.53
21245A0201	JAKINAPALLI CHANDHANA	
21245A0206	VEMULA SATYANARAYANA	8.43
20241A0248	UMMIDISETTY NIHARIKA	
20241A0257	SUSANI NEHA	8.28
21245A0205	SANATHANA JAHNAVI	

Class coordinator HOD,EEE

III B.Tech - I Sem (EEE)

SEC TIO N	Course s	PSA	PE	MPMC	EHV	PS Lab	PE Lab	MPMC Lab	Cloud Computi ng (moocs
	Course codes	GR20A3012	GR20A3012	GR20A3012	GR20A3012	GR20A3012	GR20A3012	GR20A3012	GR20A3012
	TOTAL	65	65	65	65	65	65	65	
	PASS	50	50	47	60	63	64	64	
	PASS(76.92%	76.92%	72.30%	92.30%	96.92%	98.46%	98.46%	
A	FACU LTY NAME	Dr J Sridevi	Dr Pakkir aiah B	Dr D Raveen dra	Dr D G Padhan	Dr J Sridevi / V Usha Rani/ U Vijaya Lakshmi	Dr Pakkiraiah B/ G Sandhya Rani	Dr P Srividya Devi/ M N Sandhya Rani	P Ravikan th
	FACU LTY ID	516	1593	1604	1283	516/1045/692	1593/888	931/882	1178

Dr Phaneendra Babu B

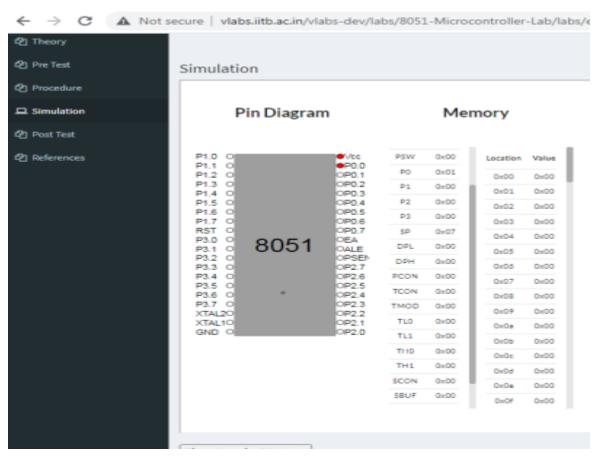
Class coordinator HOD,EEE



Stakeholders Feedback for the Microprocessors and Microcontrollers Lab Action Taken Report

According to the student's feedback: Less rating for the point: Rate your Teachers Guidance in other activities like NPTEL, MOOCs etc.

For these students are guided to experiments in Virtual labs of course Microprocessors and Microcontrollers Lab by IIT Bombay

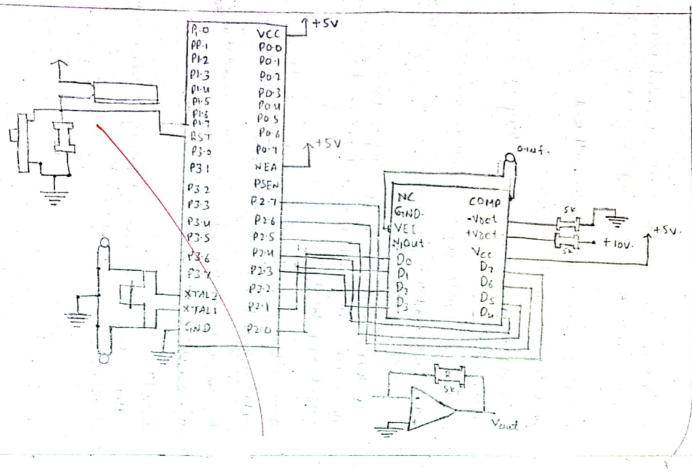


Students are instructed to do in Virtual MOOCs platforms: Supporting documents attached.

113

and

DAC



Aim; Interfacing ADC and DAC. 2 8051 micaocontaollea with

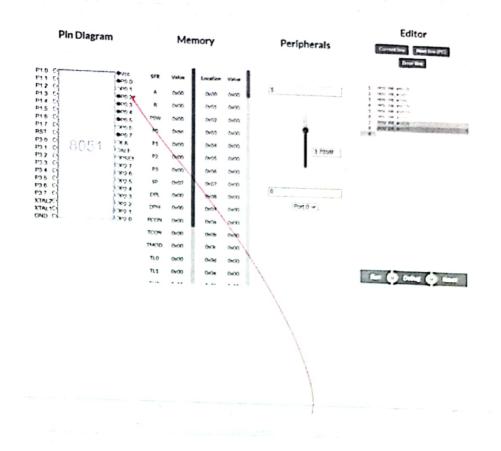
Apparatus: virtual labs.

; worload DAC sample program: MOV PO, # OFFh 302 300 300 300 MOV MOV MOV PO, # 20h Po, # OBDh Po, # OFFh. PU,#10h p0, # 08h PO, # OEFh PO, # OAFh

procedure:

2. Read all the instauctions popping up from simulator 1.60 to simulator section to perform an experiment window carefully.

3. clickon show samples button provided below. 4. copy program from samples and poste in text understand the interfacing diagram and sample code editor in simulator window.



section.

5 on write your own assembly language code to forth editor
6. select appropriate part according to your code
13. To check syntar errors on each line, debug option
13. If your code output is depending on time sequence
14. If your code output is depending on time sequence
15. If your code output is depending on time sequence
16. If your code output is depending on time sequence
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Hence we can observe the interfacing of 8051
Microcontroller with ADC and DAC

11. Submit the answer and PDF will be generated.

10. solve Test questions given below the simulator

9.70 get the final output, sun the simulation ofter

debugging and assembling the code with no error

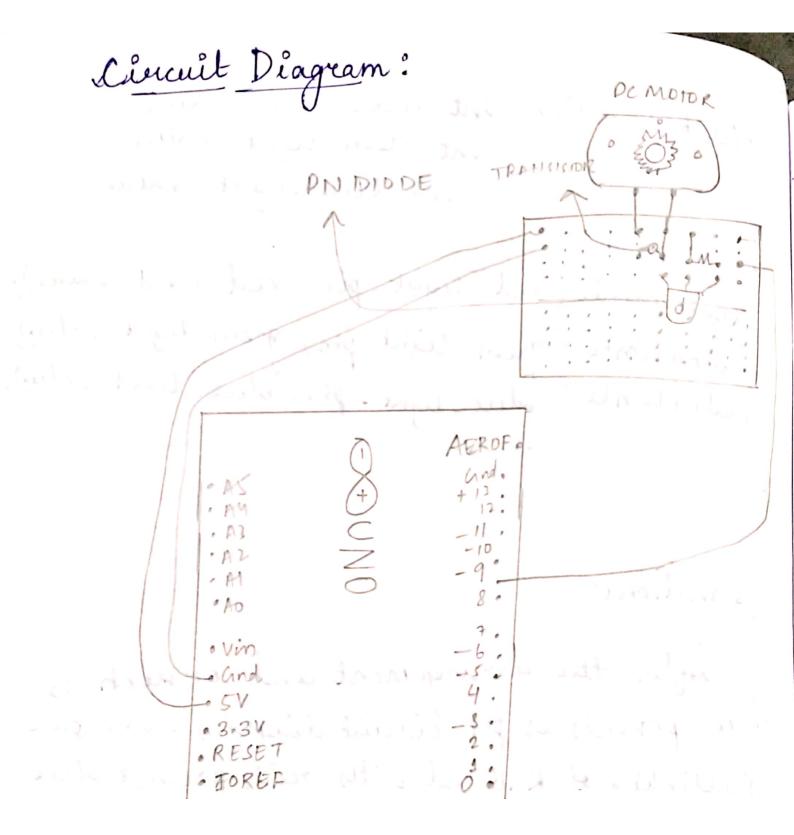
DC MOTOR CONTROL

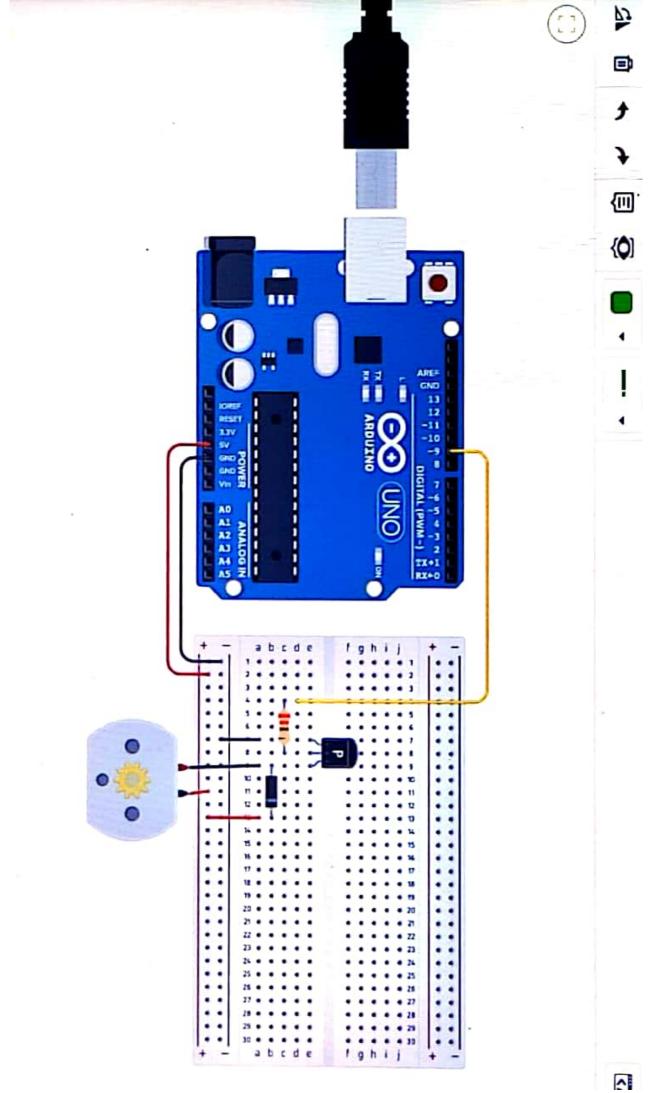
Aim: To control the speed of Oc motor using arduino and rotation also.

```
Apparatus:
Ardeino UND
DC motor
Breadboard
Transisor
Resistor
PN diode
Arduino IDE
```

```
Program code:
 void setup ()
  pin mode (5, OUTPUT);
  pin Mode (6, OUTPUT);
  pin Mode (8, OUTPUT);
   pin Mode (9, OUTPUT);
   pin Mode (10, OUTPUT);
  pin Mode (11, OUTPUT);
  digital Write (5, HIGH);
  digital Write (6, HIGH),
  void loop ()
 // Clockwise rotation
  digital Write (8, HIGH);
 dégital Write (9, LOW);
```

```
delay (3000);
  Stop
   digital Write (8, LOW);
   digital Write (9, LOW);
    dilay ($000);
  // Anti-clockwise rotation
    digital Write ( &, LOW):
    digital Write (9, HIGH);
    delay (3000);
 11 Stop
   digital Write (8,10W);
    digital Write (9, LOW);
   delay (1000);
 Observation:
  When the above code is executed in
 ardumo DDE, the DC motor rotates in
 clockwise, anti-clockwise and the
     speed of de motor is controlled
   according to the given speed.
Result:
    The de motor is controlled
     using arduino respectively.
```





According to the student's feedback Less overall rating for Lab

Inspite of the VLabs, MCQs are conducted for the students to improve their performance in lab timings



Signatures of Faculty members

HoD